

Abstracts

GaAs HBT profile optimization using the Taguchi method

G.N. Henderson and Der-woei Wu. "GaAs HBT profile optimization using the Taguchi method." 1997 MTT-S International Microwave Symposium Digest 3. (1997 Vol. III [MWSYM]): 1465-1468.

A method is presented for quantitatively optimizing HBT performance using a combination of physical and equivalent circuit models in the design-of-experiments framework. An optimized profile was designed which demonstrated over 4 dB improvement in Gmax at 10 GHz (over the M/A-COM baseline process) without resorting to reduced device dimensions and/or a thin base layer.

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